

IC-705 PTT output buffer board. VK3PE 20th April, 2021

This PCB is based on an idea by VK3AFW but also has provision for a relay contact output option if required.

Ron's original Sch 'suffered' from a condition that if the IC-705 is not powered up, the output of this PCB would have been active LOW, ie a PTT condition.

While normally probably not an issue, I have added to the V2 version a few additional parts to prevent this. I am yet to try this but believe it should work.

For now though, I only have the V1 version, dated Mar 29th 2021. Due to a blunder on my part, I inadvertently reversed the TIP and RING connections to the 3.5mm jack which connects to the IC-705 via a plug-plug cable.

It's possible to get around this by simply reversing the connections on the cable but this is not a good way to do it as the cable becomes non-standard and it's easy to forget in time that the cable has been altered. Far easier is to correct the PCB by cutting one track and adding a short wire link on the bottom. (A 'V2' PCB will be made later to correct this error)



Track cut:



Wire link using some wire wrap wire in this case:

