

# T41 QSD BOARD (Rx)

DRAWN BY VK3PE 10TH JULY 2022

Clock (VFO) from main board.

edFrom input, filtered

I & Q out to Main board.

See Group msg# 17472  
July 27th 2022

POWER INPUT

PCB sent to jlc 28th july 2025  
2807-22 ADDED FREQ EQUALISATION ON RF AMP CR4 & R4A

Title <b>T41 QSD V009</b>		DRAWN BY VK3PE 10TH JULY 2022	
Size: A3	Number:	Revision: B010 FROM V009	(C) VK3PE 2022
File:	Date: 31-Jul-2022	Time: 12:48:09	Sheet 0 of 0